

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	non-planar adj3 transistor with fin	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:39
L2	5	non-planar adj3 transistor with body	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:40
L3	0	2 and pull-up and pull-down	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:40
L4	0	2 and pullup and pulldown	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:40
L5	3	2 and (sram or static)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:42
L6	3105511	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:42
L7	177328	6 and (sram or static)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:55
L8	766	7 and single adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:44
L9	66	8 and (transistor or cell) with single adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:45
L10	3	9 and multi\$5 adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:49
L11	27610	single adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:49
L12	330	finfet	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:49

L13	27921	11 12	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:49
L14	301	13 and transistor with single adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:50
L15	16	14 and transistor with multi\$5 adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:50
L16	1	15 and (pullup or pull-up)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:51
L17	1	15 and (pulldown or pull-down)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:52
L18	802	7 and (finfet or single adj3 (fin or body))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:53
L19	2	18 and cell with multi\$5 adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:53
L20	640	7 and (finfet or single adj2 (fin or body))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:56
L21	1	20 and (pull-up or pullup) with (finfet or single adj2 (fin or body))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:58
L22	1	21 and multi\$5 adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 09:58

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L1	1931	365/154.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:06
L2	3	1 and single adj3 (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:07
L3	1	1 and finfet	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:09
L4	0	1 and (tri adj gate or tri-gate) with transistor with (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:10
L5	18	(tri adj gate or tri-gate) with transistor with (fin or body)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:12
L6	1	5 and (pullup or pull-up) and (pulldown or pull-down)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:12
L7	1	(tri adj gate or tri-gate) with transistor with (fin or body) with cell	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:12
L8	422478	static or sram	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:12
L9	1	8 and (tri-gate or tri adj gate) with transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/16 10:13